

**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Cancelled)

2. (Cancelled)

3. (Original) A semiconductor integrated circuit for outputting an OR output of an external interrupt factor and an internal interrupt factor as an interrupt request signal to an outside, comprising:

switching means provided in an I/O power source section in which supply of a power is continuously carried out also after an internal power source is shut-off and bypassing an output end of the interrupt request signal to an input end of the external interrupt factor by a switching operation interlocked with the breaking of the internal power source; and

control means for controlling the output end of the interrupt request signal to a high impedance when the internal power source is shut-off.

4. (Original) A semiconductor integrated circuit for outputting an OR output of an external interrupt factor and an internal interrupt factor as an interrupt request signal to an outside, comprising:

switching means provided in an I/O power source section in which supply of a power is continuously carried out also after an internal power source is shut-off and bypassing an output end of the interrupt request signal to an input end of the external interrupt factor by a switching operation for responding to an external control signal; and

control means for controlling the output end of the interrupt request signal to a high impedance when the external control signal is supplied.

5. (Cancelled)

6. (Cancelled)

7. (Original) An interrupt request output method of a semiconductor integrated circuit for outputting an OR output of an external interrupt factor and an internal interrupt factor as an interrupt request signal to an outside,

wherein an output end of the interrupt request signal is controlled to a high impedance and the output end of the interrupt request signal and an input end of the external interrupt factor are bypassed through switching means to be closed when an internal power source is shut-off during the breaking of the internal power source, and an interrupt request signal supplied to the input end of the external interrupt factor is output to the outside.

8. (Original) An interrupt request output method of a semiconductor integrated circuit for outputting an OR output of an interrupt request signal sent from an external interrupt factor and an internal interrupt factor as an interrupt request signal to an outside,

wherein an output end of the interrupt request signal is controlled to a high impedance and the output end of the interrupt request signal and an input end of the external interrupt factor are bypassed through switching means to be closed when an external control signal is supplied during the supply of the external control signal, and an interrupt request signal supplied to the input end of the external interrupt factor is output to the outside.